

Listing of the Claims

Please accept amended claims 13, 16, and 18 as follows:

1.(Original) An array substrate comprising:

a gate line;

a data line crossing the gate line to define a pixel region that includes first and second regions;

a switching device that is electrically connected to the gate line and the data line;

a transmissive electrode that is electrically connected to the switching device, the transmissive electrode being formed in the first region;

a reflective electrode that is electrically insulated from the transmissive electrode, the reflective electrode being formed in the second region that is adjacent to the first region; and

a compensating wiring that is electrically connected to the switching device, wherein the compensating wiring facing the reflective electrode in the second region with an insulation layer interposed between the compensating wiring and the reflective electrode.

2. (Original) The array substrate of claim 1, wherein the switching device corresponds to a thin film transistor comprising a gate electrode that is electrically connected to the gate line, a source electrode that is electrically connected to the data line, and a drain electrode that is electrically connected to the transmissive electrode and the compensating wiring.

3. (Original) The array substrate of claim 1, wherein the compensating wiring and the data line are formed from a same layer.

4. (Original) An array substrate comprising:

a first gate line;

a second gate line that is electrically insulated from the first gate line;

a data line crossing the first and second gate lines to define a pixel region that includes first and second regions;

a first switching device that is electrically connected to the first gate line and the data line;

a second switching device that is electrically connected to the second gate line;

a transmissive electrode that is electrically connected to the second switching device, the transmissive electrode being formed in the first region;

a reflective electrode that is electrically insulated from the transmissive electrode, the reflective electrode being formed in the second region that is adjacent to the first region; and

a compensating wiring that is electrically connected to the first switching device, the compensating wiring facing the reflective electrode and the transmissive electrode with an insulation layer interposed between the compensating wiring and the reflective electrode and between the compensating wiring and the transmissive electrode.

5. (Original) The array substrate of claim 4, wherein the first switching device corresponds to a first thin film transistor including a gate electrode that is electrically connected to the second gate line, a source electrode that is electrically connected to the data line, and a drain electrode that is electrically connected to the first compensating wiring.

6. (Original) The array substrate of claim 4, wherein the second switching device corresponds to a second thin film transistor including a gate electrode that is electrically connected to the first gate line, a source electrode that is electrically connected to a ground voltage, and a drain electrode that is electrically connected to the transmissive electrode.

7. (Original) The array substrate of claim 6, further comprising a third thin film transistor that includes a gate electrode that is electrically connected to the first gate line, a source electrode that is electrically connected to the data line, and a drain electrode that is electrically connected to the compensating wiring.

8. (Original) The array substrate of claim 4, wherein the second switching device

corresponds to the second thin film transistor including a gate electrode that is electrically connected to the first gate line, a source electrode that is electrically connected to the data line, and a drain electrode that is electrically connected to the transmissive electrode and the compensating wiring.

9. (Original) The array substrate of claim 4, further comprising a circuit for allowing the first gate line to maintain a first driving signal until the second gate line receives a second driving signal.

10. (Original) The array substrate of claim 4, wherein the compensating wiring and the data line are formed from a same layer.

11. (Original) A liquid crystal display apparatus comprising:

i) a first substrate including:

a gate line;

a data line crossing the gate line to define a pixel region that includes first and second regions;

a switching device that is electrically connected to the gate line and the data line;

a transmissive electrode that is electrically connected to the switching device, the transmissive electrode being formed in the first region;

a reflective electrode that is electrically insulated from the transmissive electrode, the reflective electrode being formed in the second region that is adjacent to the first region; and

a compensating wiring that is electrically connected to the switching device, the compensating wiring facing the reflective electrode in the second region with an insulation layer interposed between the compensating wiring and the reflective electrode;

ii) a second substrate including a common electrode that faces the transmissive electrode and the reflective electrode; and

iii) a liquid crystal layer interposed between the first and second substrates.

12. (Original) A liquid crystal display apparatus comprising:

- i) a first substrate including:
- a first gate line;
 - a second gate line that is electrically insulated from the first gate line;
 - a data line crossing the first and second gate lines to define a pixel region that includes first and second regions;
 - a first switching device that is electrically connected to the first gate line and the data line;
 - a second switching device that is electrically connected to the second gate line;
 - a transmissive electrode that is electrically connected to the second switching device, the transmissive electrode being formed in the first region;
 - a reflective electrode that is electrically insulated from the transmissive electrode, the reflective electrode being formed in the second region that is adjacent to the first region; and
 - a compensating wiring that is electrically connected to the first switching device, the compensating wiring facing the reflective electrode and the transmissive electrode with an insulation layer interposed between the compensating wiring and the reflective electrode and between the compensating wiring and the transmissive electrode;
- ii) a second substrate including a common electrode that faces the transmissive electrode and the reflective electrode; and
- iii) a liquid crystal layer interposed between the first and second substrates.

13. (Currently Amended) A liquid crystal display apparatus comprising:
- a first substrate including first and second switching devices, a transmissive electrode and a reflective electrode, a source electrode and a gate electrode of the first switching device being electrically connected to a data line and a gate line, respectively, a source electrode of the second switching device being electrically connected to a drain electrode of the first switching device, the transmissive electrode and the reflective electrode being electrically connected to the drain electrode of the first switching device and a drain electrode of the second switching devices respectively or reverse;
 - a second substrate including a common electrode that faces the first and second electrodes; and

- a liquid crystal layer interposed between the first and second substrates.
14. (Original) The liquid crystal display device of claim 13, wherein the first switching device corresponds to a first NMOS transistor including a first electrode that is electrically connected to the data line, a second electrode that is electrically connected to the gate line, and a third electrode that is electrically connected to the transmissive electrode, and the second switching device corresponds to a second NMOS transistor including a fourth electrode that is electrically connected to the third electrode, a fifth electrode that is electrically connected to the gate line, and a sixth electrode that is electrically connected to the reflective electrode.
15. (Original) The liquid crystal display device of claim 13, wherein a first voltage that is applied to the reflective electrode is lower than a second voltage that is applied to the transmissive electrode.
16. (Currently Amended) A liquid crystal display device comprising:
a first substrate including a switching device that is electrically connected to a data line and a gate line, a transmissive electrode that is electrically connected to the switching device, a reflective electrode that is electrically connected to the transmissive electrode, and a metal wiring facing the reflective electrode with an insulation layer interposed between the metal wiring and the reflective electrode;
a second substrate including a common electrode facing the transmissive electrode and the reflective electrode; and
a liquid crystal layer that is interposed between the first and second substrates;
17. (Original) The liquid crystal display apparatus of claim 16, wherein the gate line protrudes to form the metal wiring that faces the reflective electrode.
18. (Currently Amended) As liquid crystal display apparatus comprising:
a first substrate including a pixel region having a switching device,
a plurality of pixel electrodes electrically connected to the switching device;

a second substrate including a plurality of common electrodes, formed from a same layer and separated from each other, each of the common electrodes corresponding to each of the pixel electrodes; and

a liquid crystal layer that is interposed between the first and second substrates.

19. (Original) The liquid crystal display apparatus of claim 18, wherein the pixel electrodes comprise:

a transmissive electrode that allows a first light provided from a backside of the first substrate to transmit the transmissive electrode; and

a reflective electrode that reflects a second light provided from a front side of the second substrate.

20. (Original) The liquid crystal display apparatus of claim 19, wherein the common electrodes comprise:

a first common electrode that faces the transmissive electrode; and

a second common electrode that is electrically insulated from the first common electrode, the second common electrode facing the reflective electrode.

21. (Original) The liquid crystal display apparatus of claim 20, wherein a first voltage is applied to the first common electrode, and a second voltage that is lower than the first voltage is applied to the second common electrode.